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MEMC 00-1400(2806.1)  
PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of Robert J. Falster et al.

Art Unit 1765

Serial No. 10/039,196

Filed January 2, 2002

Confirmation No. 7221

For PROCESS FOR PREPARING SINGLE CRYSTAL SILICON HAVING  
IMPROVED GATE OXIDE INTEGRITY

March 6, 2002

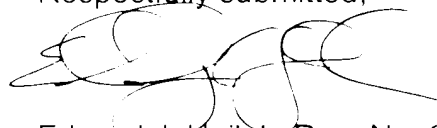
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Washington, D.C. 20231

**SUPPLEMENTAL APPLICATION DATA SHEET**

SIR:

Applicants listed some incorrect inventor information on the Application Data Sheet filed on January 2, 2002 respecting the above-referenced application. There was also a misspelling on the filing receipt received. In accordance with 37 CFR 1.76(d)(3) & (4), Applicants respectfully submit herewith a Supplemental Application Data Sheet. Applicants also respectfully request a corrected filing receipt reflecting this correction.

Respectfully submitted,



Edward J. Hejlek, Reg. No. 31,525  
SENNIGER, POWERS, LEAVITT & ROEDEL  
One Metropolitan Square, 16th Floor  
St. Louis, Missouri 63102  
(314) 231-5400

EJH/dep  
\*Enclosure



## **SUPPLEMENTAL APPLICATION DATA SHEET**

### **Application Information**

<u>Serial No.::</u>	<u>10/039,196</u>
<u>Filing Date::</u>	<u>January 2, 2002</u>
Application Type::	Non-Provisional
Subject Matter::	Utility
Title::	Process For Preparing Single Crystal Silicon Having Improved Gate Oxide Integrity
Attorney Docket Number::	MEMC 00-1400(2806.1)
Request for Early Publication::	No
Request for Non-Publication::	No
Total Drawing Sheets::	17
Small Entity::	No
Petition Included::	No
Secrecy Order in Parent::	No

### **Applicant Information**

Applicant Authority Type::	Inventor
Primary Citizenship Country::	US
Status::	Full Capacity
Given Name::	Robert
<u>Middle Initial::</u>	<u>J.</u>
Family Name::	Falster
City of Residence::	London
Country of Residence::	England
<u>Street of Mailing Address::</u>	<u>P.O. Box 8, 501 Pearl Drive</u>
City of Mailing Address::	St. Peters
State of Mailing Address::	MO
<u>Postal Code of Mailing Address::</u>	<u>63376</u>

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Russia
Status::	Full Capacity
Given Name::	Vladimir
<u>Middle Initial::</u>	<u>V.</u>
Family Name::	Voronkov
City of Residence::	Merano
Country of Residence::	Italy
<u>Street of Mailing Address::</u>	<u>Via Nazionale, 59</u>
<u>City of Mailing Address::</u>	<u>Merano</u>
<u>State or Province of Mailing</u>	
<u>Address::</u>	<u>Italy</u>
<u>Postal Code of Mailing Address::</u>	<u>39012</u>

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Italy
Status::	Full Capacity
Given Name::	Paolo
Family Name::	Mutti
<u>City of Residence::</u>	<u>Milan</u>
Country of Residence::	Italy
<u>Street of Mailing Address::</u>	<u>Via G. Leopardi N. 3</u>
<u>City of Mailing Address::</u>	<u>Milan</u>
<u>State or Province of Mailing</u>	
<u>Address::</u>	<u>Italy</u>
<u>Postal Code of Mailing Address::</u>	<u>20123</u>

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Italy
Status::	Full Capacity
<u>Given Name::</u>	<u>Francesco</u>
Family Name::	Bonoli

City of Residence:: Novara  
Country of Residence:: Italy  
Street of Mailing Address:: Viale Gherzi, 31  
City of Mailing Address:: Novara  
State or Province of Mailing  
Address:: Italy  
Postal Code of Mailing Address:: 28100

**Correspondence Information**

Correspondence Customer Number:: 000321

**Representative Information**

Representative Customer Number:: 000321

**Domestic Priority Information**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This application	Non-Provisional of	60/259,362	01/02/02

**Assignee Information**

Assignee Name:: **MEMC Electronic Materials, Inc.**